

FIG. 1

105

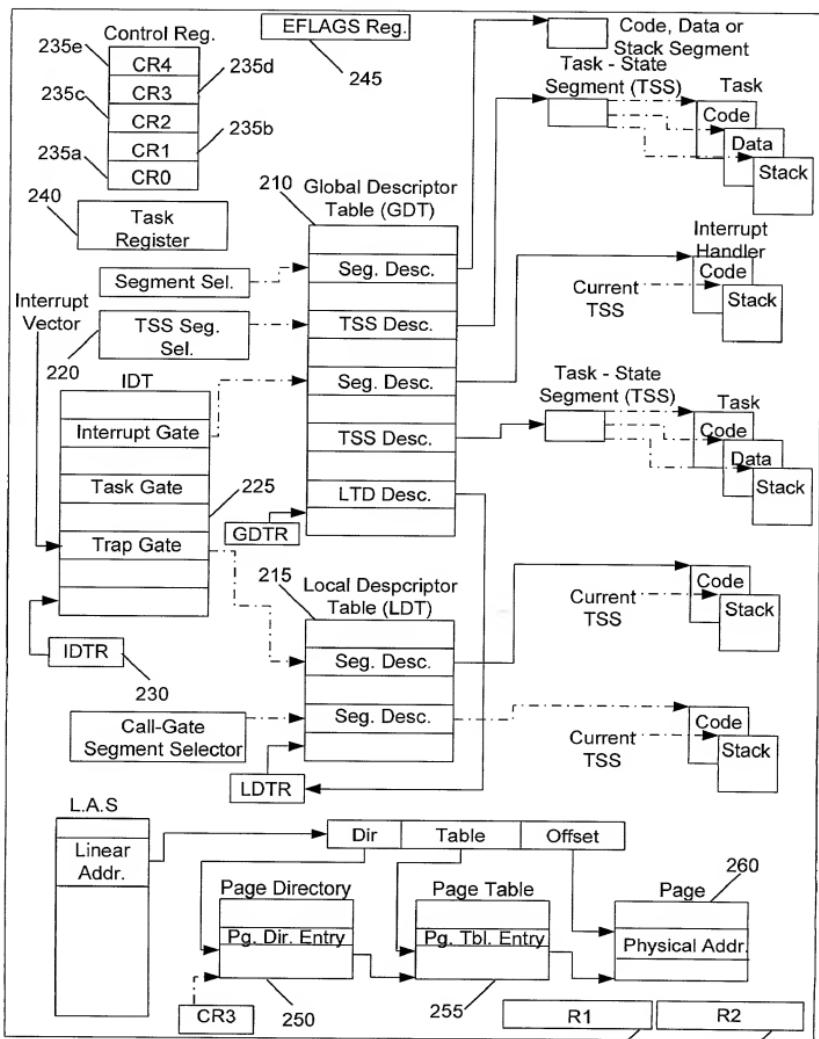


FIG. 2

PRIVILEGED INSTRUCTIONS	ACCESS
350 access to CR3 register	operating system access only
360 access to global descriptor table	operating system and device drivers access only
365 access to interrupt descriptor table register	global access
370 access to task register	operating system and device drivers access only
370	● ● ●

FIG. 3

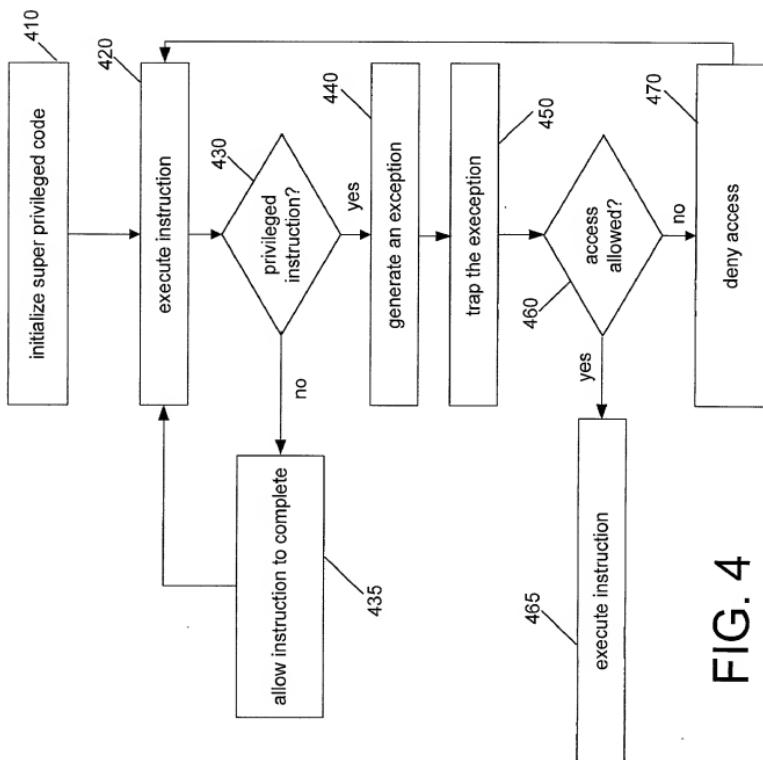


FIG. 4

MEMORY LOCATION	READ ONLY	WRITE ONLY	ACCESS RIGHTS
0			
1		● ● ●	
	● ● ●		
1000			OS
1001			OS
1002			OS
1003			OS
	● ● ●	● ● ●	

515 →

520 →

530 →

540 →

510 →

FIG. 5

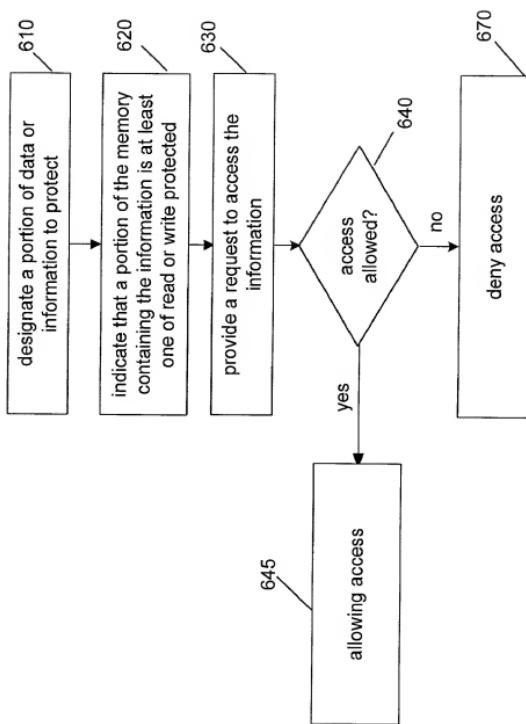


FIG. 6